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SECODE

Compiler-based automation of side-channel countermeasures

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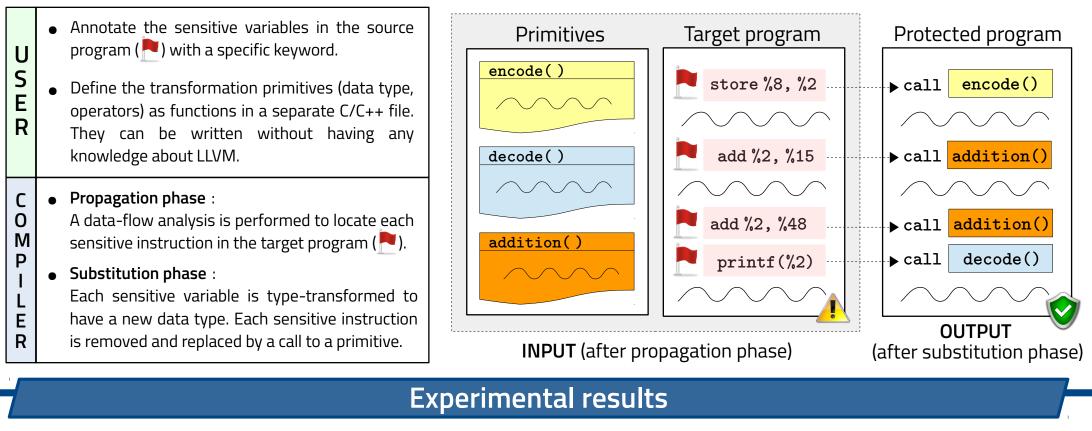
Compiler-automated transformation

insert software The idea is to protections against physical threats (e.g. masking, error correcting codes) automatically during compilation.

Advantages of compile-time transformation :

- Automation of the whole process (no source or assembly editing)
- Invisible for the software programmer (like compiler optimizations)
- Low-level control over the target program (e.g. memory allocation)
- Coverage of most Instruction Set Architectures (ARM, AVR, x86, ...)

Reconfigurable countermeasures



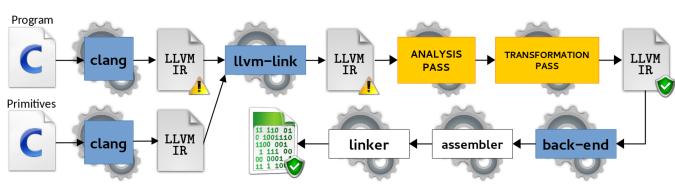
Application of Boolean Masking on an AES-128 implementation

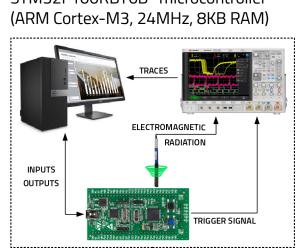
<u>Setup</u> :

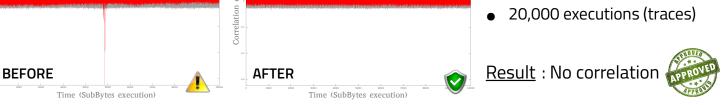
STM32F100RBT6B microcontroller

Side-channel analysis :

 Correlation Power Analysis (1st order) on AES 1st round







Future work

Study the implementability of other transformations with primitives, such as :

- Direct Sum Masking (DSM), Inner Product Masking (IP)
- Error-correcting codes, Linear Complementary Dual codes (LCD)



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Other SECODE partners are listed on the right.







